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FIG. 1

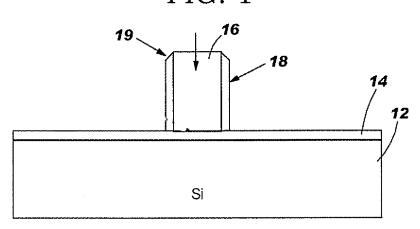


FIG. 2

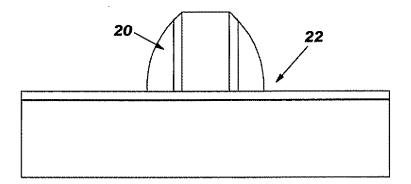
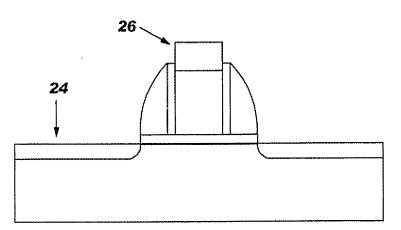
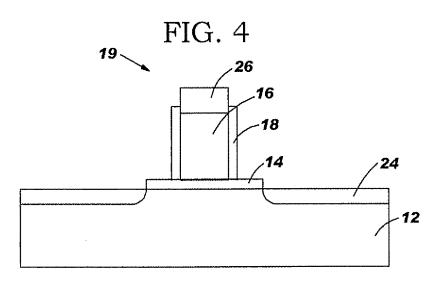


FIG. 3



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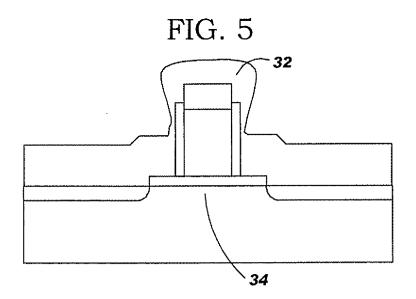
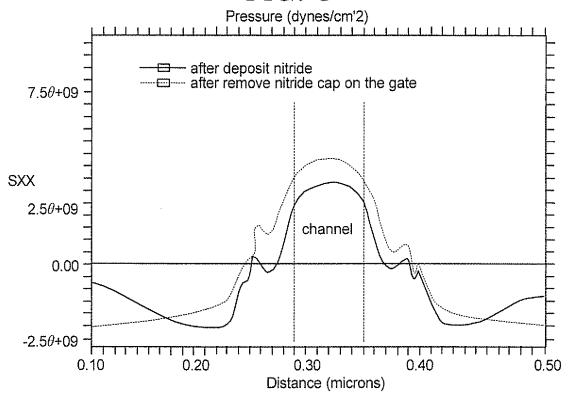


FIG. 6



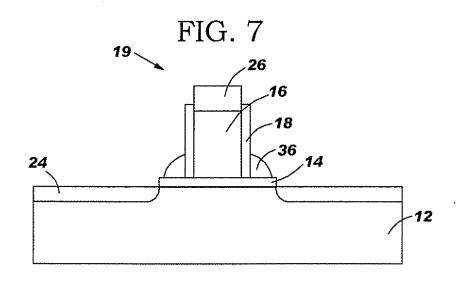




FIG. 8

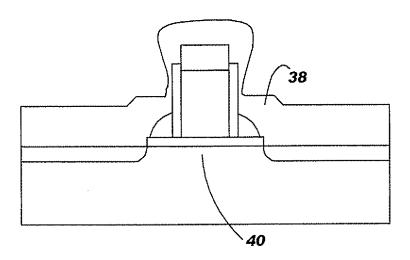
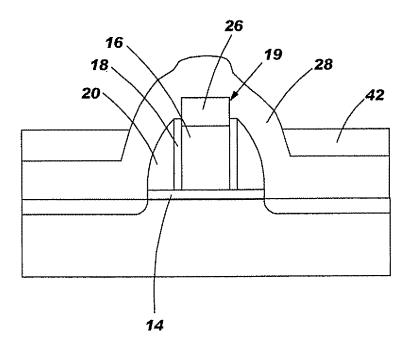


FIG. 9



Application No. 10/707,842 Confirmation No. 1841
Applicant: Haining S. YANG, et. al. Attorney Docket No. P27165
Title: METHOD AND APPARATUS FOR INCREASE STRAIN EFFECT IN A TRANSISTOR CHANNEL

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6/7 FIG. 10

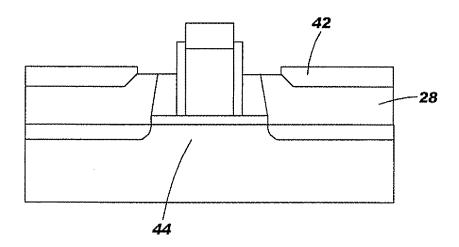


FIG. 11

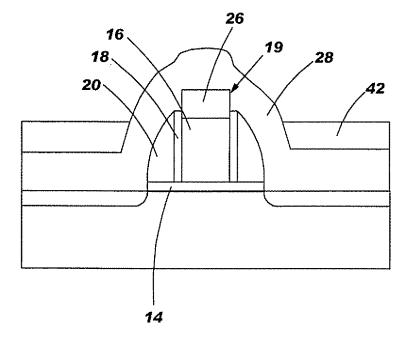


FIG. 12

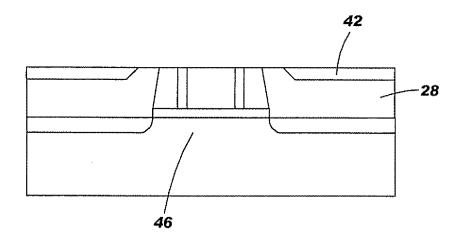


FIG. 13

